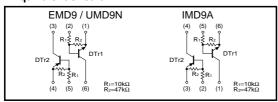
Digital Transistor (Dual Digital Transistors for Inverter Drive)

EMD9 / UMD9N / IMD9A

Features

1) DTA114Y and DTC114Y transistors are built-in a EMT or UMT or SMT package.

●Equivalent circuit



Package, marking, and packaging specifications

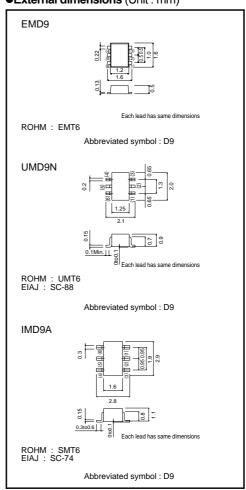
Туре	EMD9	UMD9N	IMD9A
Package	EMT6	UMT6	SMT6
Marking	D9	D9	D9
Code	T2R	TR	T108
Basic ordering unit (pieces)	8000	3000	3000

●Absolute maximum ratings (Ta=25°C)

Parameter		Symbol	Limits	Unit	
Supply voltage		Vcc	50	V	
Input voltage		VIN	-6 to +40	V	
Output current		lo	70	mA	
Collector current		Ic (Max.)	100	mA	
Power dissipation	EMD9, UMD9N	Pd	150(TOTAL)	mW *1	
	IMD9A	Fu	300(TOTAL)	mW *2	
Junction temperature		Tj	150	°C	
Storage temperature		Tstg	-55 to +150	°C	

*1 120mW per element must not be exceeded. PNP type negative symbols have been omitted *2 200mW per element must not be exceeded. PNP type negative symbols have been omitted.

●External dimensions (Unit : mm)



●Electrical characteristics (Ta=25°C)

	,					
Parameter	Symbol	Min.	Тур.	Max.	Unit	Conditions
Input voltage	V _{I(off)}	-	-	0.3	.,	Vcc=5V , Io=100mA
	VI(on)	1.4	-	-	V	Vo=0.3V , Ii=1mA
Output voltage	Vo(on)	-	0.1	0.3	V	lo=5mA , l≔0.25mA
Input current	lı	-	-	0.88	mA	Vi=5V
Output current	IO(off)	-	-	0.5	mA	Vcc=50V , Vi=0V
DC current gain	Gı	68	-	-	-	Io=5mA , Vo=5V
Transition frequency *	f⊤	-	250	-	MHz	Vce=10V , Ie= -5mA , f=100MHz
Input resistance	R ₁	7	10	13	kW	-
Resistance ratio	R ₂ /R ₁	3.7	4.7	5.7	_	_

PNP type negative symbols have been omitted.

●Electrical characteristics curves DTr1 (DTC114Y)

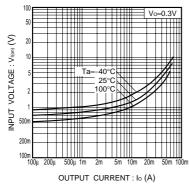


Fig.1 Input voltage vs. output current (ON characteristics)

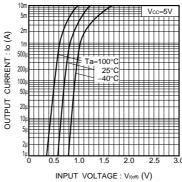


Fig.2 Output current vs. input voltage (OFF characteristics)

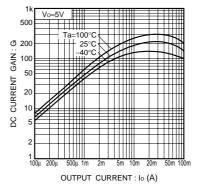


Fig.3 DC current gain vs. output current

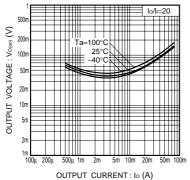


Fig.4 Output voltage vs. output current

●Electrical characteristics curves DTr2 (DTA114Y)

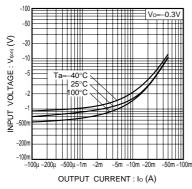


Fig.1 Input voltage vs. output current (ON characteristics)

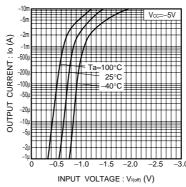


Fig.2 Output current vs. input voltage (OFF characteristics)

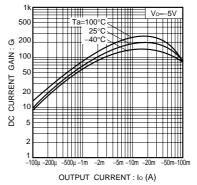


Fig.3 DC current gain vs. output current

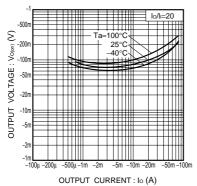


Fig.4 Output voltage vs. output current

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